

REMARKS

Claims 2 and 9 are rewritten in independent form.

Double Patenting

Claim 13 is rejected under statutory-type double patenting. Claim 13 is amended to reverse the order of the first transistor and first inductor. Thus, the rejection is overcome.

Claims 1-10 are rejected based on the judicially created doctrine of obviousness-type double patenting. Applicant will submit a terminal disclaimer to overcome this rejection upon indication of otherwise allowable claims.

Claim Rejections - 35 USC § 102

Claims 1 and 4-8 are rejected under 35 USC 102(e) as being anticipated by U.S. Patent No. 5,789,799 to Voinigescu et al. ("Voinigescu"). Applicant disagrees with the Examiner's specific interpretations with respect to Voinigescu. Nonetheless, claims 1 and 4-8 are cancelled.

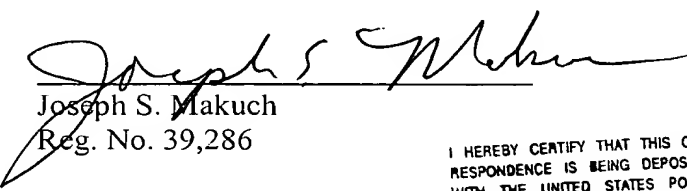
New Claims

New claims 15-26 are added. Support for these claims can be found in the specification at page 2, lines 30-31 and page 20, line 24 through page 21, line 11.

Applicant requests reconsideration in view of the foregoing amendments and remarks. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

2. (Amended once) An RF mixer [according to claim 1] comprising:
a mixer core having an LO port for receiving an LO signal, an IF port for providing an IF output signal, and an input port having an input terminal for receiving a current signal; and
an RF input section coupled to the input terminal for providing the current signal
responsive to an RF input signal;

wherein the RF input section includes:

a transistor coupled to the input terminal, and

an inductor coupled to the transistor to extend the dynamic range of the mixer;

wherein:

the transistor includes a first terminal coupled to the input terminal, a second terminal coupled to receive a reference signal, and a third terminal; and

the inductor includes a first terminal coupled to the third terminal of the transistor and a second terminal coupled to receive the RF input signal.

9. (Amended once) An RF mixer [according to claim 5] comprising:
a mixer core having a first input terminal for receiving a first current signal and a second input terminal for receiving a second current signal;
a first subcell coupled to the first input terminal of the mixer core to provide the first current signal to the mixer core responsive to an RF input signal, the first subcell having a first transistor and a first inductor coupled to the first transistor to extend the dynamic range of the mixer; and

a second subcell coupled to the second input terminal of the mixer core to provide a second current signal to the mixer core responsive to an RF input signal, the second subcell having a second transistor and a second inductor coupled to the first transistor to extend the dynamic range of the mixer;

wherein:

the first transistor includes a first terminal coupled to the first input terminal, a second terminal coupled to receive a reference signal, and a third terminal; and

the inductor includes a first terminal coupled to the third terminal of the transistor and a second terminal coupled to receive the RF input signal.

13. (Amended once) A current mirror comprising:

a first transistor having a first terminal [for receiving an input signal,] and a second terminal coupled [to the first terminal of the first transistor] together to cause the first transistor to operate as a diode, and a third terminal coupled to a common node;

a first inductor coupled between an input terminal and the [third] first terminal of the first transistor [and a common node] to reduce the noise of the current mirror;

a second transistor having a first terminal for transmitting an output signal, a second terminal coupled to the [first terminal of the first transistor] input terminal, and a third terminal; and

a second inductor coupled between the third terminal of the second transistor and a common node to reduce the noise of the current mirror.

Claims 15-26 are new.